

REMARKS

The Office Action of July 5, 2005 has been carefully considered. In response thereto, the claims have been amended as set forth above. Reconsideration and allowance in view of the foregoing amendments and the following remarks is respectfully requested.

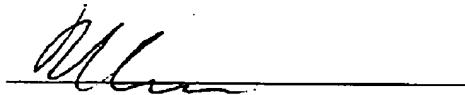
In particular, claim 9 has been amended to recite that a buffer manager is configured to assert a wrap signal when a first buffer operation involves consecutively accessing each buffer location of a block of buffer locations assigned sequential address values in an order different than an order defined by the sequential address values, and is further configured to limit accesses to the buffer of a second buffer operation in dependence upon the wrap signal.

Claim 14 has been amended to recite determining a block address and an offset address corresponding to a first buffer operation involving consecutively accessing each buffer location of a block of buffer locations assigned sequential block address values, determining when the offset address is non-sequential relative to the block address wherein the buffer locations are accessed in an order different than an order defined by the sequential address values, and limiting access of a second buffer operation to locations within the block when the offset address is non-sequential.

The foregoing features are not believed to be taught or suggested by the cited references.

Allowance of claims 9-16 is respectfully requested.

Respectfully submitted,



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